



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s) Allen et al.  
Docket No.: YOR920030175US1  
Serial No.: 10/661,041  
Filing Date: September 12, 2003  
Group: 2811  
Examiner: Cuong Q. Nguyen

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature:  Date: July 16, 2004

Title: Techniques for Patterning Features in Semiconductor Devices

TRANSMITTAL LETTER

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith is a Response to Restriction Requirement relating to the above-identified patent application. There is no additional claim fee due in connection with the Response.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **IBM Corporation Deposit Account No. 50-0510** as required to correct the error. Duplicate copies of this letter are enclosed.

Respectfully submitted,

  
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Dated: July 16, 2004



IFW

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5

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Signature: *Vina M. Munn* Date: July 16, 2004

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15

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
20 P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

25 In response to the outstanding Office Action dated June 24, 2004 in the above-identified application, Applicants submit the following remarks.